年　　　級：博 7（102入學）

著作列表

Journal Papers

1. **Chin-Yu Sun**, Allen C.-H. Wu, TingTing Hwang, "A Novel Privacy-Preserving Deep Learning Scheme without a Cryptography Component," has been accepted in *Computers & Electrical Engineering*, January 2021.

2. **Chin-Yu Sun**, Hsiao Ling Wu, Hung-Min Sun, TingTing Hwang, "A New Attack to Self-Certified Digital Signature for E-commerce Applications," has been accepted for publication in *Journal of Information Science and Engineering*, June 2020.

3. Chin-Chen Chang, Hsiao-Ling Wu, **Chin-Yu Sun**, "Notes on "Secure authentication scheme for IoT and cloud servers"," *Pervasive and Mobile Computing*, vol. 38, pp. 275-278, July 2017.

4. Yanjun Liu, Chin-Chen Chang, **Chin-Yu Sun**, "Notes on "An Anonymous Multi-server Authenticated Key Agreement Scheme Based on Trust Computing Using Smart Card and Biometrics"," *International Journal of Network Security*, Vol. 18, No. 5, pp. 997-1000, September 2016.

5. Yanjun Liu, Chin-Chen Chang, **Chin-Yu Sun**, "A Novel Three-party Authenticated Key Exchange Protocol Based on Secret Sharing," *Journal of Information Hiding and Multimedia Signal Processing*, Vol. 7, No. 4, pp. 741-753, July 2016.

6. Hsiao–Ling Wu, Chin–Chen Chang, **Chin–Yu Sun**, "A secure authentication scheme with provable correctness for pay–TV systems," *Security and Communication Networks,* Vol. 9, No.11, pp. 1577-1588, July 2016.

7. **Chin-Yu Sun** and Ching-Chun Chang, "Cryptanalysis of a Secure and Efficient Authentication Scheme for Access Control in Mobile Pay-TV Systems," *International Journal of Network Security*, Vol. 18, No. 3, pp. 594-596, May 2016.

8. Chin-Chen Chang, **Chin-Yu Sun**, Shih-Chang Chang, "A Strong RSA-based and Certificateless-based Signature Scheme," *International Journal of Network Security*, Vol. 18, No. 2, pp. 201-208, March 2016.

9. Yanjun Liu, Chin-Chen Chang, **Chin-Yu Sun**, "A secure and efficient scheme for digital gift certificates," *Journal of Information Hiding and Multimedia Signal Processing*, Vol. 6, No. 3, pp. 416-429, May 2015.

10. Chin−Chen Chang, **Chin−Yu Sun**, Ting−Fang Cheng, "A dependable storage service system in cloud environment," *Security and Communication Networks*, Vol. 8, No. 4, pp. 574-588, March 2015.

11. Chin-Chen Chang, Yeh-Chieh Chou, **Chin-Yu Sun**, "Novel and practical scheme based on secret sharing for laptop data protection," *IET Information Security*, Vol. 9, No. 2, pp. 100-107, March 2015.

12. Ching-Chun Chang and **Chin-Yu Sun**, "A secure and efficient authentication scheme for e-coupon systems" *Wireless personal communications*, Vol. 77, No. 4, pp. 2981-2996, March 2014.

年　　級：博七 (103入學)

著作列表

Journal Papers

[1] **<u>Yuan-Ming Chang</u>**, Chia-Yu Sung, Yu-Chien Sheu, Meng-Hsun Yu, Min-Yih Hsu, Jenq-Kuen Lee. "Support NNEF Execution Model for NNAPI", *The Journal of Supercomputing* (2021): 1-32, Springer.

[2] **<u>Yuan-Ming Chang</u>**, Wei-Cheng Liao, Shao-Chung Wang, Chun-Chieh Yang, Yuan-Shin Hwang. "A framework for scheduling dependent programs on GPU architectures", *Journal of Systems Architecture,* Volume 106, June 2020, Elsevier.

[3] Chun-Chieh Yang, Shao-Chung Wang, Min-Yi Hsu, **<u>Yuan-Ming Chang</u>**, Yuan-Shin Hwang and Jenq-Kuen Lee. "Support OpenCL 2.0 Compiler on LLVM for PTX Simulators", *Journal of Signal Processing Systems*, Volume 91, Issue 3-4 (2019): pp 261-271, Springer.

[4] **<u>Yuan-Ming Chang</u>**, Shao-Chung Wang, Chun-Chieh Yang, Yuan-Shin Hwang, and Jenq-Kuen Lee. "Enabling PoCL-Based Runtime Frameworks on the HSA for OpenCL 2.0 Support", *Journal of Systems Architecture*, Volume 81, November 2017, Pages 71-82, Elsevier.

Conference Papers

[1] Yi-Ru Chen, Chia-Hsuan Chang, Hui-Hsin Liao, CC Lin, Chao-Lin Lee, **<u>Yuan-Ming Chang</u>**, Chun-Chieh Yang, I-Wei Wu, Heng-Kuan Lee, Jenq-Kuen Lee. "Support TVM QNN Flow on RISC-V with SIMD Computation", *RISC-V Global Forum*, Virtual, Sep 2020 (Lightning talk).

[2] Yi-Ru Chen, Hui-Hsin Liao, Chia-Hsuan Chang, Che-Chia Lin, Chao-Lin Lee,, **<u>Yuan-Ming Chang</u>**, Chun-Chieh Yang, Jenq-Kuen Lee. "Experiments and Optimizations for TVM on RISC-V Architectures with P Extension", *2020 International Symposium on VLSI Design, Automation and Test (VLSI-DAT)*, Hsinchu, Taiwan, 2020, pp. 1-4.

[3] Allen Lu, Piyo Chen, Heng Lin, Chun-Chieh Yang, Ssu-Hsuan Lu, **<u>Yuan-Ming Chang</u>**, Shao-Chung Wang, Charlie Su, Chen-Ling Chou, Vin Sharma, and Jenq-Kuen Lee. "Experiments and AI Model Validations for Neo/TVM on RISC-V Architectures with SIMD", *RISC-V Summit*, San Jose, Dec 2019 (Poster).

[4] Jenq-Kuen Lee, Chun-Chieh Yang, Allen Lu, Piyo Chen, **<u>Yuan-Ming Chang</u>**,

CH Chang, Yi-Ru Chen, HH Liao, Chao-Lin Lee, Ssu-Hsuan Lu, and Shao-Chung Wang. "Supporting TVM on RISC-V Architectures with SIMD Computations", *TVM and Deep Learning Compiler Conference*, Seattle, Dec 2019 (lightning talk and slides).

[5] Allen Lu, Chao-Lin Lee, **Yuan-Ming Chang**, Piyo Chen, Hsiang-Wei Sung, Heng Lin, Shao-Chung Wang, and Jenq-Kuen Lee. "Enabling TVM on RISC-V Architectures with SIMD Instructions", *RISC-V Forum*, March 2019 (Oral presentation).

[6] Jenq-Kuen Lee, Allen Lu, **Yuan-Ming Chang**, Chao-Lin Lee, Piyo Chen, and Shao-Chung Wang. "Supporting TVM on RISC-V Architectures", *TVM and Deep Learning Compiler Conference*, Seattle, Dec 2018 (lightning talk and slides).

[7] Shih-Huan Chien, **Yuan-Ming Chang**, Chun-Chieh Yang, Yuan-Shin Hwang, and Jenq-Kuen Lee. 2018. "Graph Support and Scheduling for OpenCL on Heterogeneous Multi-core Systems", *In Proceedings of the 47th International Conference on Parallel Processing Companion* (ICPP '18). Association for Computing Machinery, Article 14, 1–7. (ICPPEMS, Oregon)

[8] Chun-Chieh Yang, Shao-Chung Wang, Min-Yih Hsu, **Yuan-Ming Chang**, Yuan-Shin Hwang, Jenq-Kuen Lee. "OpenCL 2.0 Compiler Adaptation on LLVM for PTX Simulators", *2017 46th International Conference on Parallel Processing Workshops* (ICPPW), Bristol, 2017, pp. 53-58. (ICPP EMS)

[9] Hsiang-Wei Sung, **Yuan-Ming Chang**, Shao-Chung Wang, Jenq-Kuen Lee. "OpenCV Optimization on Heterogeneous Multi-core Systems for Gesture Recognition Applications", *2016 45th International Conference on Parallel Processing Workshops* (ICPPW), Philadelphia, PA, USA, 2016, pp. 59-65. (ICPP EMS)

年　　級：博四（105 入學）

著作列表

Journal Papers

1. **<u>Yen-Ting Chen</u>**, Ming-Chang Yang, Yuan-Hao Chang, Tseng-Yi Chen, Hsin Wen Wei, and Wei-Kuan Shih, "Co-Optimizing Storage Space Utilization and Performance for Key-Value Solid State Drives," IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems (TCAD), vol. 38, no. 1, pp. 29-42, Jan. 2019.
   [CATEGORIES: SCIE, Impact factor = 2.168 (2019), 5 years Impact factor = 2.236]

2. Shuo-Han Chen, **<u>Yen-Ting Chen</u>**, Yuan-Hao Chang, Hsin-Wen Wei, and Wei-Kuan Shih, "A Progressive Performance Boosting Strategy for 3D Charge-trap NAND Flash," IEEE Transactions on VLSI Systems (TVLSI), vol. 26, no. 11, pp. 2322-2334, Nov. 2018.
   [CATEGORIES: SCIE, Impact factor = 2.037 (2019), 5 years Impact factor = 1.903]

Conference Papers

1. **<u>Yen-Ting Chen</u>**, Ming-Chang Yang, Yuan-Hao Chang, and Wei-Kuan Shih, "Parallel-Log-Single-Compaction-Tree: Flash-Friendly Two-Level Key-Value Management in KVSSDs," 2020 25th Asia and South Pacific Design Automation Conference (ASP-DAC), Beijing, China, 2020, pp. 277-282

2. **<u>Yen-Ting Chen</u>**, Ming-Chang Yang, Yuan-Hao Chang, Tseng-Yi Chen, Hsin Wen Wei, and Wei-Kuan Shih, "KVFTL: Optimization of storage space utilization for key-value-specific flash storage devices," 2017 22nd Asia and South Pacific Design Automation Conference (ASP-DAC), Chiba, Japan, 2017, pp. 584-590.

3. Shuo-Han Chen, **<u>Yen-Ting Chen</u>**, Hsin-Wen Wei, and Wei-Kuan Shih, "Boosting the Performance of 3D Charge Trap NAND Flash with Asymmetric Feature Process Size Characteristic," 54th ACM/IEEE Design Automation Conference (DAC), Austin, USA, Jun. 18-22, 2017. (Top Conference)