102 年 2 月 通過 學術審查

94 學年度入學 (博七)

Journal Papers

1. Shih-Hsiang Lo, Che-Rung Lee, I-Hsin Chung, and Yeh-Ching Chung, "PBIG: Optimizing Pairwise Box Intersection Checking on GPUs for Large-Scale Simulations," accepted by *ACM Transactions on Modeling and Computer Simulation*. [**Microsoft Academic Search**: rank 2/14 in Computer Simulation] [**Journal Citation Reports**: 5-year Impact Factor: 1.36; rank 61/245 in mathematics, applied; 55/99 in computer science, interdisciplinary applications; rank 9/23 in ACM Transactions]

2. Shih-Hsiang Lo, Che-Rung Lee, I-Hsin Chung, and Yeh-Ching Chung, "Improving GPU Memory Performance with Artificial Barrier Synchronization," major revision and submitted to *IEEE Transactions on Parallel and Distributed Systems*. [**Journal Citation Reports**: 5-year Impact Factor: 1.78; rank 18/99 in computer science, theory & methods, SCI, EI]

3. Che-Rung Lee, Shih-Hsiang Lo, I-Hsin Chung, and Yeh-Ching Chung, " Applying Data Streaming and Compression Techniques for Data Movement Cost Reduction and Beyond in GPU-based Systems," submitted to *Parallel Computing*. [**Journal Citation Reports**: 5-year Impact Factor: 1.228; rank 20/99 in computer science, theory & methods, SCI, EI]

Conference Papers

1. Che-Rung Lee, Shih Hsiang Lo, Nan-Hsi Chen, Yeh-Ching Chung, and I-Hsin Chung, "GPU Performance Enhancement via Communication Cost Reduction: Case Studies of Radix Sort and WSN Relay Node Placement Problem," Proceedings of *the 12th IEEE/ACM International Symposium on Cluster*, *Cloud*, *and Grid Computing* (CCGRID 2012), pp. 132-139. [Class A conference in distributed computing, acceptance rate: 27%=83/302] (source: in 2009 Australian Ranking of ICT Conference)

2. <u>Shih-Hsiang Lo</u>, Che-Rung Lee, I-Hsin Chung, and Yeh-Ching Chung, "A Parallel Rectangle Intersection Algorithm on GPU+CPU," *Proceedings of the 11th IEEE/ACM International Symposium on Cluster, Cloud, and Grid Computing* (CCGRID 2011), pp. 43-52. [Class A conference in distributed computing, acceptance rate: 29%=55/189] (source: in 2009 Australian Ranking of ICT Conference) [google scholar citation count:2]

3. <u>Shih-Hsiang Lo</u>, Fang-Ping Pai and Yeh-Ching Chung, "Offloading Region Matching of Data Distribution Management with CUDA," *Proceedings of IEEE International Conference on Intelligent Systems, Modeling and Simulation* (ISMS 2010), pp. 306-311.

4. <u>Shih-Hsiang Lo</u>, Cheng-An Chiu, Ding-Yong Hong, Fang-Ping Pai and Yeh-Ching Chung, "MGRID: A Modifiable-Grid Region Matching Approach for DDM in the HLA RTI," *Proceedings of ACM High Performance Computing Symposium of 2009 Spring Simulation Multiconference*, (SpringSim 2009), No. 114.