

102年3月 通過 學術審查

96 學年度入學 (博六)

Journal paper

1. **Yoshi S.C. Huang**, C.K. Chou, and C.T. King, "Application-Driven End-to-End Traffic Predictions for Low Power NoC Design," IEEE Transactions on Very Large Scale Integration Systems (TVLSI), Vol. 21, No. 2, pages 229-238, Feb. 2013 (SCI, EI, Impact factor = 1.219)
2. T.M. Chung, **Yoshi S.C. Huang**, C.T. King, and C.P. Chang, "Optimizing Upload Bandwidth for Quality of VCR Operations in P2P VoD Systems," International Journal of Ad Hoc and Ubiquitous Computing (IJAHUC) 2010 - Vol. 5, No.4 pp. 201 – 208 (SCI, Impact factor = 0.848)

Conference and workshop paper

1. **Yoshi S.C. Huang**, Yu-Chi Chang, Tsung-Chan Tsai, Yuan-Ying Chang, and Chung-Ta King, "Attackboard: A Novel Dependency-Aware Traffic Generator for Exploring NoC Design Space," in the Proceedings of the 49th Design Automation Conference (DAC), San Francisco, CA, June 2012. (164/741)
2. **Yoshi S.C. Huang**, Kaven C.K. Chou, C.T. King, and S.Y. Tseng, "NTPT: On the End-to-End Traffic Prediction in the On-Chip Networks," In Proceedings of the 47th Design Automation Conference (DAC), Anaheim, CA, June 2010. (148/607)
3. **Yoshi S.C. Huang**, Huan-Yu Liu, Yuan-Ying Chang, and C.T. King, "Floodgate: Application-Driven Flow Control in Network-on-Chip for Many-Core Architectures," accepted by the 4th International Workshop on Network on Chip Architectures (NoCArc'11) held in conjunction with the 44th Annual IEEE/ACM International Symposium on Microarchitecture (MICRO-44), Dec. 2011.
4. Yuan-Ying Chang, **Yoshi S.C. Huang**, Matthew Poremba, Vijaykrishnan Narayanan, Yuan Xie, and Chung-Ta King, "TS-Router: On Maximizing the Quality-of-Allocation in the On-Chip Network", accepted by the 19th IEEE International Symposium on High-Performance Computer Architecture (HPCA-19). (51/249)

5. Yuan-Ying Chang, **Yoshi S.C. Huang**, Vijaykrishnan Narayanan, and Chung-Ta King, "ShieldUS: A Novel Design of Dynamic Shielding for Eliminating 3D TSV Crosstalk Coupling Noise," accepted by the 18th Asia and South Pacific Design Automation Conference (ASP-DAC) (97/311)
6. Y.F. Hung, S.Y. Tseng, C.T. King, H.Y. Liu, and **Yoshi S.C. Huang**, "Parallel Implementation and Performance Prediction of Object Detection in Videos on the Tiler Many-core Systems", Proc. of International Symposium on Pervasive Architectures, Algorithms, and Networks (I-SPAN), Dec. 2009.

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Journal Paper

1. **Ding-Yong Hong**, Jan-Jan Wu, Pen-Chung Yew, Wei-Chung Hsu, Chun-Chen Hsu, Pangfeng Liu, Chien-Min Wang, and Yeh-Ching Chung. “Efficient and Retargetable Dynamic Binary Translation on Multicores.” *To appear in IEEE Transactions on Parallel and Distributed Systems (TPDS)*. (5-Year Impact Factor: 1.780)

Conference Paper

1. **Ding-Yong Hong**, Chun-Chen Hsu, Pen-Chung Yew, Jan-Jan Wu, Wei-Chung Hsu, Yeh-Ching Chung, Pangfeng Liu and Chien-Min Wang. “HQEMU: A Multi-Threaded and Retargetable Dynamic Binary Translator on Multicores.” In *IEEE/ACM International Symposium on Code Generation and Optimization (CGO)*, March 2012, pp. 104-113. (*Top conference in compilers*)
2. **Ding-Yong Hong**, Fang-Ping Pai, Shih-Hsiang Lo and Yeh-Ching Chung. “A Scalable HLA RTI System Based on Multiple-FedServ Architecture.” In *International Conference on Computer Modelling and Simulation (UKSim)*, March 2010, pp. 527-532.
3. Shih-Hsiang Lo, Cheng-An Chiu, Fang-Ping Pai, **Ding-Yong Hong** and Yeh-Ching Chung. “MGRID: A Modifiable-Grid Region Matching Approach for DDM in the HLA RTI.” In *Spring Simulation Multiconference (SpringSim)*, March 2009.
4. **Ding-Yong Hong**, Ching-Wen You and Yeh-Ching Chung. “An Efficient MPI-IO for Noncontiguous Data Access over InfiniBand.” In *International Symposium on Parallel Architectures, Algorithms, and Networks (I-SPAN)*, December 2005, pp. 140-147.

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Journal Papers

1. **Wu-Chun Chung**, Chin-Jung Hsu, Kuan-Chou Lai, Kuan-Ching Li, and Yeh-Ching Chung, "Direction-Aware Resource Discovery in Large-Scale Distributed Computing Environments," *The Journal of Supercomputing* (In press).
2. **Wu-Chun Chung**, Yi-Hsiang Lin, Kuan-Chou Lai, Kuan-Ching Li, and Yeh-Ching Chung, "A Self-Adaptive Resource Index and Discovery System in Distributed Computing Environments," *International Journal of Ad Hoc and Ubiquitous Computing*, vol. 10, no. 2, 2012, pp 74-83.
3. Chin-Jung Hsu, **Wu-Chun Chung**, Kuan-Chou Lai, Kuan-Ching Li, and Yeh-Ching Chung, "Cooperative Failure Detection in Multi-Overlay Environments," *Journal of Internet Technology*, vol. 12, no. 2, 2011, pp. 259-267.
4. **Wu-Chun Chung**, Chin-Jung Hsu, Yi-Hsiang Lin, Kuan-Chou Lai, and Yeh-Ching Chung, "G2G: A Meta-Grid Framework for the Convergence of P2P and Grids," *International Journal of Grid and High Performance Computing*, vol. 2, no. 3, 2010, pp. 1-16.
5. **Wu-Chun Chung** and Ruay-Shiung Chang, "A New Mechanism for Resource Monitoring in Grid Computing," *Future Generation Computer Systems*, vol. 25, no. 1, 2009, pp. 1-7.

Conference Papers

1. **Wu-Chun Chung**, Chin-Jung Hsu, Kuan-Chou Lai, Kuan-Ching Li, and Yeh-Ching Chung, "Direction-Aware Resource Discovery Service in Large-Scale Grid and Cloud Computing," in: *Proceedings of the 2011 IEEE International Conference on Service-Oriented Computing and Applications (SOCA)*, Irvine, CA, USA, December 2011.
2. Chin-Jung Hsu, **Wu-Chun Chung**, Kuan-Chou Lai, Kuan-Ching Li, and Yeh-Ching Chung, "A Novel Approach for Cooperative Overlay-Maintenance in Multi-overlay Environments," in: *Proceedings of the 2010 IEEE Second International Conference on Cloud Computing Technology and Science (CloudCom)*, Indianapolis, Indiana, USA, November 2010, pp. 81-88.

3. Yi-Hsiang Lin, **Wu-Chun Chung**, Kuan-Chou Lai, Kuan-Ching Li, and Yeh-Ching Chung, "SARIDS: A Self-Adaptive Resource Index and Discovery System," in: *Proceedings of the 10th IEEE International Symposium on Pervasive Systems, Algorithms, and Networks (I-SPAN)*, Kaohsiung, Taiwan, December 2009, pp. 521-526.

4. **Wu-Chun Chung**, Chin-Jung Hsu, Yi-Shiang Lin, Kuan-Chou Lai, and Yeh-Ching Chung, "G2G: A Meta-Grid Framework for the Convergence of P2P and Grids," in: *Proceedings of the 4th International Conference on Advances in Grid and Pervasive Computing (GPC), Lecture Notes In Computer Science (LNCS)*, Geneva, Switzerland, May 2009, pp. 131-141.

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Journal Papers

1. A君, 共同作者, 共同作者 and 共同作者, "Efficient Programming Paradigm for Video Stream Processing on the TILE64 Platform," *The Journal of Supercomputing*, Springer. Published online: 24 January 2013.
2. 共同作者, A君, and 共同作者, "Hardware Supported Multicast in Fat-Tree-Based InfiniBand Networks," *The Journal of Supercomputing*, Springer, Vol. 40, No. 3, June 2007
3. A君 and 共同作者, "Master-Worker Model for MapReduce Paradigm on the TILE64 Many-core Platform," *Future Generation Computer Systems*, Elsevier. (Submitted on 03 Jan 2013, currently under review).

Conference Papers

1. A君, 共同作者, 共同作者, 共同作者, and 共同作者, "An Efficient Programming Paradigm for Shared-Memory Master-Worker Video Decoding on TILE64 Many-Core Platform," *Proceedings of IEEE of International Conference on Parallel Processing (ICPP)*, Taipei, Taiwan, September 2011
2. A君, 共同作者, 共同作者, 共同作者, 共同作者, and 共同作者, "Parallelization of Motion JPEG Decoder on TILE64 Many-core Platform", *Proceedings of The 2nd Russia-Taiwan Symposium on Methods and Tools of Parallel Programming Multicomputers (MTPP 2010)*, *Lecture Notes in Computer Science (LNCS) 6083*, Vladivostok, Russia, May 2010
3. 共同作者, A君, and 共同作者, "Reducing Leakage Power of JPEG Image on Asymmetric SRAM", *Proceedings of IEEE International Conference on Embedded and Ubiquitous Computing (EUC-09)*, Vancouver, Canada, August 2009
4. 共同作者, 共同作者, A君, and 共同作者, "SCRF-A Hybrid Register File Architecture," *Proceedings of International Conference on Parallel Computing Technologies (PaCT)*, *Lecture Notes in Computer Science (LNCS) 4671*, Pereslavl-Zalessky, Russia, September 2007

5. 共同作者, A君, and 共同作者, "A Tree-Turn Model for Irregular Networks," *Proceedings of IEEE International Symposium on Network Computing and Applications (NCA)*, USA, July 2006
6. 共同作者, 共同作者, A君, and 共同作者, "An Efficient and Low Overhead Random Forwarding Table Construction Method for Deadlock-Free Routing Algorithms," *Proceedings of IASTED International Conference on Parallel and Distributed Computing and Systems (PDCS)*, USA, November 2005
7. 共同作者, A君, 共同作者, and 共同作者, "Multicast in Fat-Tree-Based InfiniBand Networks," *Proceedings of IEEE International Symposium on Network Computing and Applications (NCA)*, USA, July 2005
8. A君, 共同作者, and 共同作者, "A Multiple LID Routing Scheme for Fat-Tree-Based InfiniBand Networks," *Proceedings of IEEE International Parallel and Distributed Processing Symposiums (IPDPS)*, USA, April 2004.