

102 年 6 月 通過 學術審查

96 學年度入學 (博六)

Journal Papers:

1. **Fong-Yuan Chang**, Ren-Song Tsay, Wai-Kei Mak and Sheng-Hsiung Chen: MANA: A Shortest Path MAze Algorithm under Separation and Minimum Length NANometer Rules, " IEEE TCAD 2013. (Accepted)

Conference Papers:

1. **Fong-Yuan Chang**, Ren-Song Tsay, Wai-Kei Mak, Sheng-Hsiung Chen: A separation and minimum wire length constrained maze routing algorithm under nanometer wiring rules. ASP-DAC 2013: 175-180 (acceptance rate: 31%)
2. **Fong-Yuan Chang**, Sheng-Hsiung Chen, Ren-Song Tsay, Wai-Kei Mak: Cut-demand based routing resource allocation and consolidation for routability enhancement. ASP-DAC 2011: 533-538 (acceptance rate: 35%)
3. **Fong-Yuan Chang**, Ren-Song Tsay, Wai-Kei Mak: How to consider shorts and guarantee yield rate improvement for redundant wire insertion. ICCAD 2009: 33-38 (acceptance rate 26.3%)

Patent

1. **Fong-Yuan Chang**, Wei-Shun Chuang, Sheng-Hsiung Chen, Hsian-Ho Chang, Ruey-Shi Rau, "Multiple level spine routing," Springsoft May 2012: US 20120137265
2. **Fong-Yuan Chang**, Wei-Shun Chuang, Sheng-Hsiung Chen, Hsian-Ho Chang, Ruey-Shi Rau, "Multiple level spine routing,"Springsoft May 2012: US 20120137264
3. **Fong-Yuan Chang**, Wai-Kei Mak, Ren-Song Tsay,"Method for improving yield rate using redundant wire insertion."Springsoft December 2012: US 8336001

4. Hsin-Po Wang, Yu-Sheng Lu, **Fong-Yuan Chang**, Yi-Der Lin, Sung-Han Tsai, Ru Lin Yang, Chun-Cheng Chi, Hsueh Liang Hsu, "System for implementing post-silicon IC design changes," Springsoft USA September 2011: US 8015522
5. **Fong-Yuan Chang**, Sheng-Hsiung Chen, Tung-Chieh Chen, Ren-Song Tsay, Wai-Kei Mak, "Systems and methods for designing and making integrated circuits with consideration of wiring demand ratio," Springsoft June 2011: US 20110154282
6. **Fong-Yuan Chang**, Wai-Kei Mak, Ren-Song Tsay, "Method for Improving Yield Rate Using Redundant Wire Insertion," Springsoft May 2011: US 20110107278
7. Hsin-Po Wang, Yu-Sheng Lu, **Fong-Yuan Chang**, Yi-Der Lin, Sung-Han Tsai, Ru Lin Yang, Chun-Cheng Chi, Hsueh Liang Hsu, "System for implementing post-silicon ic design changes." Springsoft Usa July 2009: US 20090178013
8. **Fong-Yuan Chang**, Ing-Kai Huang, "Website service method," May 2003: US 20030088484
9. **Fong-Yuan Chang**, "Option cell placement method." (Filed)
10. **Fong-Yuan Chang**, Sheng-Hsiung Chen, "A methodology for routing compaction." (Filed)

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Journal Papers

1. **Chi-Bang Kuan**, Jia-Jhe Li, Chung-Kai Chen, and Jenq Kuen Lee, "C++ Support and Applications for Embedded Multicore DSP Systems", Accepted, *Journal of Signal Processing Systems*, Springer.
2. **Chi-Bang Kuan** and Jenq Kuen Lee, "Compiler Supports for VLIW DSP Processors with SIMD Intrinsics", *Concurrency and Computation: Practice and Experience*, Vol. 24, Issue 5, pp. 517-532, Wiley, 2012.

Book Chapters

1. Jenq Kuen Lee, Rong-Guey Chang, and **Chi-Bang Kuan**, "Compiler Techniques for Array Languages", *Encyclopedia of Parallel Computing*, pp. 75-87, Springer, 2011, Padua, David (Ed.), ISBN 978-0-387-09765-7, Price: 1200 €.

Conference Papers

1. **Chi-Bang Kuan**, Shao-Chung Wang, Wen-Li Shih, Kun-Hsien Tsai, Shang-Hong Lai, and Jenq Kuen Lee, "Parallelization of a Bokeh Application on Embedded Multicore DSP Systems", *ESTImedia 2011 (ESWeek 2011)*, pp. 93-101, 2011.
2. **Chi-Bang Kuan**, Jia-Jhe Li, Chung-Kai Chen, and Jenq Kuen Lee, "C++ Compiler Supports for Embedded Multicore DSP Systems", *International Workshop on Embedded Multicore Systems (ICPP-EMS 2011)*, pp. 214-221, 2011.
3. **Chi-Bang Kuan** and Jenq Kuen Lee, "SIMD Intrinsic Supports for VLIW DSP Processors with Distributed Register Files", *Compilers for Parallel Computing 2010 (CPC 2010)*, 2010.
4. Yu-Hao Chang, **Chi-Bang Kuan**, Cheng-Yen Lin, Te-Feng Su, Chun-Ta Chen, Jyh-Shing Jang, Shang-Hong Lai, and Jenq Kuen Lee, "Support of Software Framework for Embedded Multi-core Systems with Android Environments", *ESTImedia 2011 (ESWeek 2011)*, pp. 2-8, 2011.

5. Jia-Jhe Li, **Chi-Bang Kuan**, Tung-Yu Wu, and Jenq Kuen Lee, “Enabling an OpenCL Compiler for Embedded Multicore DSP Systems”, *International Workshop on Embedded Multicore Systems (ICPP-EMS 2012)*, pp. 545-552, 2012.
6. Yu-Te Lin, **Chi-Bang Kuan**, Shao-Chung Wang, and Jenq Kuen Lee, “A Functional Approach to Optimize SIMD Computations of OpenCL Programs”, *Compilers for Parallel Computing 2012 (CPC 2012)*, 2012.
7. Cheng-Yen Lin, Po-Yu Chen, Chun-Kai Tseng, Chung-Wen Huang, Chia-Chieh Weng, **Chi-Bang Kuan**, Shih-Han Lin, Shi-Yu Huang, and Jenq Kuen Lee, “Power aware SID-based Simulator for Embedded Multicore DSP Subsystems”, *CODES+ISSS 2010 (ESWeek 2010)*, pp. 95-104, 2010.

Pending Patents

1. Jenq Kuen Lee and **Chi-Bang Kuan**, “Compiler for Providing Intrinsic Supports for VLIW DSP Processors with Distributed Register Files and Method thereof”, US Pending Patent, Publication No. US-2013-0061022-A1, Publication Date: 03/07.2013.
2. 李政崑、**關啟邦**， “一種具有分散式暫存器檔案之超長指令數位訊號處理器的內在支援提供方法及其編譯器”，中華民國專利審查中，申請案號：101131679，公開編號：201319936，公開日：2013/05/16。