# 103 年 6 月 通過 學術審查

年　　級：博七（修業九年）

## Journal Papers

1. **Chia-Han Lu**, Wen-Li Shih, Chung-Ju Wu, and Jenq Kuen Lee, "Achieving Spilling-Friendly Register File Assignment for Highly Distributed Register Files," *Journal of Supercomputing*, Accepted.
   - JCR 2012 impact factor: 0.917
   - Academic index: SCI, EI

2. Chung-Ju Wu, **Chia-Han Lu**, and Jenq Kuen Lee, "Register Spilling via Transformed Interference Equations for PAC DSP Architecture," *Concurrency and Computation: Practice and Experience*, Volume 26, Issue 3, pages 779–799, 10 March 2014.
   - JCR 2012 impact factor: 0.845
   - Academic index: SCI, EI

3. **Chia-Han Lu**, Yung-Chia Lin, Yi-Ping You, and Jenq Kuen Lee, "LC-GRFA: global register file assignment with local consciousness for VLIW DSP processors with non-uniform register files," *Concurrency and Computation: Practice and Experience*, Volume 21, Issue 1, Pages 101-114, Wiley, January 2009.
   - JCR 2008 impact factor: 1.791
   - Academic index: SCI, EI

4. Yung-Chia Lin, **Chia-Han Lu**, Chung-Ju Wu, Chung-Lin Tang, Yi-Ping You, Ya-Chaio Moo, and Jenq-Kuen Lee, "Effective Code Generation for Distributed and Ping-Pong Register Files: a Case Study on PAC VLIW DSP Cores," *Journal of VLSI Signal Processing Systems*, Volume 51, Number 3, pp. 269-288, Springer, June 2008.
   - JCR 2007 Impact factor: 0.449
   - Academic index: SCI, EI

## Conference Papers

1. Chung-Ju Wu, **Chia-Han Lu**, and Jenq Kuen Lee, "Expression

Rematerialization for VLIW DSP Processors with Distributed Register File," *Compilers for Parallel Computing 2009*, Zurich, Switzerland, Jan. 2009.

2. **Chia-Han Lu,** Yung-Chia Lin, Yi-Ping You, and Jenq Kuen Lee, "A Local-Conscious Global Register Allocator for VLIW DSP Processors with Distributed Register Files," *Compilers for Parallel Computing 2007*, Lisbon, Portugal, July 9-11, 2007.

## Patents

1. Jenq-Kuen Lee, Chung-Ju Wu, **Chia-Han Lu**, "Spilling Method in Register Files for Microprocessors," USA Patent, Application No.12/829,711, 2013.

2. Jenq-Kuen Lee, Chung-Ju Wu, **Chia-Han Lu**, "處理器之使用暫存器檔案的溢出方法," Taiwan Patent, Application No.098122720.